भारतीय सूचना प्रौद्योगिकी संस्थान, पुणे
Indian Institute of Information Technology, Pune
(An Institute of National Importance by an Act of Parliament)
Gat No - 5 & 6, Vill - Nanoli-Tarf Chakan, PO – Talegaon, Tah – Maval, Dist - Pune, Maharashtra – 410507

Admission Brochure

for

PhD admissions

under

Visvesvaraya Full-time PhD Scheme
and
Institute-funded Full-time PhD Scheme

AY 206-27

Indian Institute of Information Technology Pune

Ref No.: IIITP/PhD/FT/2026-27/01 **Dated: 31/10/2026**

1. About the Institute

Indian Institute of Information Technology Pune (IIIT Pune) is an Institute of National Importance, established in 2016 by an Act of Parliament in a Public-Private Partnership (PPP) model under the Ministry of Education, Government of India. IIIT Pune offers B.Tech. programmes in Computer Science and Engineering (CSE), Electronics and Communication Engineering (ECE), Cybersecurity, Artificial Intelligence and Data Science (AI&DS), and Microelectronics and VLSI Design; M.Tech. programmes in CSE with specialisation in AI and ECE with specialisation in VLSI Design; and PhDprogrammes in CSE, ECE, and Applied Sciences & Humanities (AS&H).

PhD Scholars will benefit from national and international research collaborations with premier institutions such as IITs, NITs, AIIMS, CSIR laboratories, C-DAC, ICMR, NFSU, and leading universities worldwide, facilitating joint supervision, knowledge exchange, and/or access to state-of-the-art research facilities.

IIIT Pune invites applications from bright, passionate, and research-driven candidates for its **Full-time PhD Programme for AY 2026–27** under two schemes:

- **Visvesvaraya Full-time PhD Scheme**
- **Institute-funded Full-time PhD Scheme**

2. About PhD Programmes:

(A) Visvesvaraya PhD Scheme:

Digital India Corporation, MeitY (Ministry of Electronics and Information Technology), and Govt. of India have granted scholarships under the scheme titled “Visvesvaraya PhD scheme for Electronics and IT: Phase-II”. Under this scheme, admissions are presently open to the full-time PhD program in the Department of **Computer Science and Engineering (CSE) and Electronics and Communication Engineering (ECE).**

(B) Institute-funded Full-time PhD Scheme:

Institute-funded Full-time PhD Scheme is an initiative of IIIT Pune to promote advanced research and innovation across **Computer Science & Engineering (CSE), Electronics & Communication Engineering (ECE), and Mathematics and Computing (M&C).** The scheme provides opportunities for aspiring researchers to pursue doctoral studies in diverse and emerging research areas, fostering interdisciplinary collaboration, scientific advancement, and technological innovation under the guidance of the Institute's faculty members.

PhD programme at IIIT Pune is designed to provide a strong foundation in the chosen research area through advanced coursework, followed by independent research culminating in the submission of a doctoral thesis. IIIT Pune offers a diverse range of research areas available for PhD students under the supervision of faculty members.

3. PhD Schemes, Eligible Departments and Seat Availability

S. No.	PhD Scheme	Eligible Disciplines	Number of Seats
1	Visvesvaraya Full-time PhD Scheme	• **Computer Science & Engineering** • **Electronics & Communication Engineering**	1
2	Institute-funded Full-time PhD Scheme	• **Computer Science & Engineering** • **Electronics & Communication Engineering** • **Mathematics and Computing**	5

4. Research Areas

Candidates may apply to work in research areas available under the supervision of faculty members in the respective departments.

Computer Science & Engineering (CSE): Artificial Intelligence, Machine Learning, Data Science, Deep Learning, Cybersecurity, Digital Forensics, Natural Language Processing, Big Data Analytics, Cloud Computing, Data Mining, Software Engineering, Formal Methods, Human–Computer Interaction, Augmented Reality, Internet of Things, Computer Networks, Mobile and Wireless Networks, Image Processing, Biometrics, Data Privacy, and related areas.

Electronics & Communication Engineering (ECE): VLSI, Microelectronics, Internet of Things, Wireless Communication, 5G and beyond, Antennas, Signal Processing, Synchronisation and Parameter Estimation, Modulation, Robotics, Cognitive Radio, NOMA, Intelligent Reflecting Surfaces (IRS), Energy Harvesting, Free-Space Optics, Machine Learning, Underwater Communication, and related areas.

Mathematics and Computing (M&C): Lattice Theory, Graph Theory, Commutative Algebra, Differential Equations, Cryptography, Applied Mathematics, and related areas.

The availability of a particular research area will depend on faculty expertise, supervisory capacity, and the Institute's requirements.

5. Eligibility Criteria

(A.) Visvesvaraya Full-time PhD Scheme:

Eligible Departments: CSE and ECE only.

Table 1: Minimum Eligibility Qualification for admission to Regular Full-time PhD Program.

S. No.	Qualifying Degree	Minimum Performance in Qualifying Degree	Additional Essential Qualification/ Requirements
1	ME/M.Tech in Relevant area	CGPA of 6.5 (6.0 for SC / ST / PwD) on a 10-point scale or 60% (55% marks for SC / ST / PwD) of marks or first class on the degree certificate	Nil
2	BE/B.Tech in Relevant area	CGPA of 6.5 (6.0 for SC / ST / PwD) on a 10-point scale or 60% (55% marks for SC / ST / PwD) of marks on the degree certificate	Completing graduation from CFTI / INI / Government Autonomous Institutes / Institute or University below 100 NIRF ranking / Foreign Institutes with QS Ranking below 1000. OR with valid GATE Score in CS/DA/EC/EE OR with minimum 3 years of relevant experience in allied fields.

(B.) Institute-funded Full-time PhD Scheme

Eligibility for the Institute-funded Full-time PhD Scheme in the CSE and ECE departments shall be as prescribed in **Table 1**.

For Mathematics and Computing Discipline:

Master's degree in basic sciences or relevant research area with Bachelor's degree in basic sciences and/or Allied Disciplines with First Class or 60% marks (55% marks for SC/ST/PwD) / CPI or CGPA of 6.5 (6.0 for SC/ST/PwD) on a 10-point scale.

Note: Under both PhD schemes, the minimum duration for PhD thesis submission shall be 3 years for candidates holding a Master's degree and 4 years for candidates holding a Bachelor's degree, subject to the applicable scheme guidelines.

6. Financial Support and Fellowship

(A.) Visvesvaraya Full-time PhD Scheme

Selected Full-time PhD scholars will be considered for financial support under the Visvesvaraya PhD Scheme for Electronics & IT: Phase II, subject to the approval of the scheme implementing agency and applicable conditions.

- **Monthly fellowship:** ₹46,250 during the first and second years and ₹52,500 during the third, fourth, and fifth years.
- **Maximum support period:** Five years or until completion of the Ph.D., whichever is earlier.
- **Reimbursement of Rent:** As admissible under the scheme, at the prescribed rates linked to the applicable city classification.

- **Research contingency support:** ₹1,20,000 per year per Full-time PhD candidate, subject to the scheme's prescribed conditions and approval procedures.
- **International conference support and visits to laboratories abroad:** Available to eligible scholars from the third year onwards, subject to the scheme's guidelines, approvals, and availability of support.

The award and continuation of fellowship and other benefits are subject to the conditions of the Visvesvaraya PhD Scheme, satisfactory academic and research progress, and approval by the competent authority.

(B.) Institute-funded Full-time PhD Scheme

- **Monthly stipend:** ₹42,000 per month during the first two years and ₹48,000 per month during the third year. The financial support may be extended for one additional year based on the recommendations of the Student Research Committee (SRC), subject to a maximum of four years from the date of PhD registration.
- Research Contingency, International Conference Support, and Visits to Laboratories Abroad: Support shall be provided as per Institute norms.
- **Global Research Opportunities –** Opportunities for research collaboration with leading international universities.
- **Expert Co-Supervision –** Opportunities to pursue research under the co-supervision of eminent experts from premier institutes and leading research laboratories.

7. Application Procedure

Interested and eligible candidates must:

- Visit the official IIIT Pune website at **https://www.iiitp.ac.in**.
- Read the applicable PhD Ordinance, scheme guidelines, eligibility criteria, and application instructions carefully.
- Complete the prescribed online application form and select the appropriate department/ discipline and funding scheme.
- Upload the required documents, including academic certificates and mark sheets, proof of date of birth, category certificate where applicable, CV, statement of purpose, research publications or experience documents where applicable, and other documents specified in the application form.
- Submit the application on or before the prescribed deadline.
- Employed, sponsored, or externally funded candidates must submit the required No Objection Certificate (NOC), sponsorship letter, or proof of funding, as applicable to their admission category.

Application links:

- **Link to apply for** Visvesvaraya Full-time PhD Scheme and Institute-funded Full-time PhD Scheme: https://iiitpadm.samarth.edu.in/index.php/site/index

Application fee: A non-refundable application processing fee of **₹826/- for GEN/OBC-NCL/EWS candidates and ₹413/- for SC/ST/PwD/Female** candidates shall be paid online through the Samarth Portal while submitting the application form.

- Please note that an application form without payment of applicable fee will not be considered.

NOTE: Candidates applying under both PhD schemes or for more than one department must submit a separate application for each scheme and/or department and pay the applicable application fee separately for each application.

8. Selection Procedure

- Applications will be scrutinised for eligibility and completeness.
- Eligible candidates will be required to appear for a written test and/or interview.
- Selection will be based on merit, suitability for the proposed research area, availability of a supervisor, the number of vacancies, and applicable scheme conditions.
- Meeting the minimum eligibility criteria does not guarantee shortlisting or admission.
- No TA/DA will be paid for attending the written test and/or interview.

Syllabus for the Written test: The question paper for written test shall be based on GATE Syllabus for respective categories as follows (including but not limited to)

For **Electronics and Communication Engineering:** Engineering Mathematics, Networks, Signals and Systems, Electronic Devices, Analog Circuits, Digital Circuits, Control Systems, Communications, Electromagnetics.

For **Computer Science and Engineering:** Engineering Mathematics, Digital Logic, Computer Organization and Architecture, Programming and Data Structures, Algorithms Theory of Computation, Compiler Design, Operating System, Databases, Computer Networks, Software Engineering.

For **Mathematics and Computing (M&C):** Abstract & Linear Algebra, Calculus, Discrete Mathematics, Lattice Theory, Graph Theory, Commutative Algebra, Differential Equations, Computational Dynamics, Optimization, Fluid Mechanics, Number Theory and Cryptography, Optimization, Probability and Statistics, Numerical Analysis, and related areas.

8. Important Guidelines

- Please read the instructions given in the brochure carefully before filling up the application

form.

- The Institute reserves the right not to fill up the seats, cancel the advertisement in whole or in part, without assigning any reason, and the decision of the Director, IIIT Pune in this regard shall be final.
- The decision of the committees involved in the selection process is final and binding. No interim correspondence/inquiries will be entertained from the candidates in connection with the process of shortlisting/selection. Any dispute with regard to the selection process will be subject to the court/tribunal having jurisdiction at Pune.
- The employed candidates are required to submit the No Objection Certificate (NOC) from the respective organization as per Annexure-I.
- Selection of candidates will be done on the basis of written test and/or Interview. No TA/DA will be paid for attending the written test and/or interview.
- Shortlisted candidates should bring the following documents at the time of written test/interview:

a. Photo ID Card/Proof
b. Receipt of the processing fee
c. Thesis / dissertation / report / publications
d. Certificates and mark-sheets along with originals and
e. Category proof

- Any queries related to admissions may be directed to academics@iiitp.ac.in
- The details regarding coursework, duration of program, theses etc. are provided in PhD Ordinances and Regulations of the Institute.
- The applicable semester fee and breakup and other details are available on the Institute website. First Semester fee at the time of admission (excluding Hostel & Mess
- Fee) is Rs. 60,200/-. Details regarding the admission fee structure are available at the link: https://www.iiitp.ac.in/docs/Fee_Structure_2026-27.pdf

9. Important Dates

Activity	Date
Release and opening of advertisement	October 31, 2026
Last date for submission of applications	November 30, 2026
Tentative date of written test and interview	December 14, 2026

Annexure-I

No Objection Certificate from Employed Candidates for PhD

(To be typed on letterhead of the Company / Organization / College / University)

Dear Sir,

This is to certify that Shri. / Smt. /Kum._____is an employee of our Company / Organization / College / University since_____ and is currently serving as_____(designation).

Our Company / Organization/ College / University has no objection to his/her application in the_____(department) to join the PhD Programme at IIIT Pune, under external / sponsored category.

Further, if selected, the Company / Organization / College / University has 'NO OBJECTION' to allow Shri. /Smt. /Kum._____

to undergo the programme of studies and also to fulfill the coursework requirements (and qualifier examination, if applicable) at IIIT Pune.

Signature & Seal of the Head of the Company / Organization / College / University

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Annexure II

STATEMENT OF PURPOSE

Statement of Purpose (SOP) is your opportunity to share with the admission committee your thoughts and feelings about research studies at IIIT Pune including your preparation for the same. Briefly describe past project/ research work done by you. Restrict yourself to 500 - 600 words. Your personal SOP will help the admission committee evaluate your application.

Applicant Name